For 3rd overtone oscillation Low frequency deviation KH9827ALy

Overview

KH9827ALy is composed of an oscillation amplifier and a three-state output buffer. Oscillation amplifier is supplied with voltage regulator output apart from VDD. The voltage supply separation builds low frequency variation with supply voltage VDD. ESD and latch-up test have complied with AEC-Q100 standard.

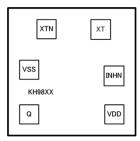
Features

Operating Voltage	1.6 to 3.63V		
Operating Temperature	−40 to 85°C		
Standby Mode INHN="L"	Q Output "Hi-z"		
	Oscillation Stopped		
INHN Input Voltage Level	C-MOS		
Q Output Duty Level	C-MOS		
Q Output Current	8mA (VDD=2.25V)		
Q Output Load (Drive Capacity)	30pF		

Device Selection Table

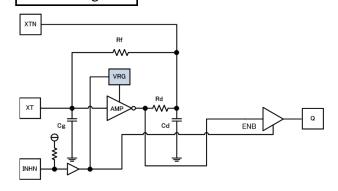
Version	Oscillation frequency (3rd Overtone)
KH9827ALA	30 to 45MHz
KH9827ALB	40 to 55MHz
KH9827ALC	50 to 75MHz
KH9827ALD	65 to 110MHz

PAD Locations



Chip Size	0.74×0.67mm
PAD Size	90×90um
Chip Thickness	130±20um
Chip Base	VSS Level

Block Diagram

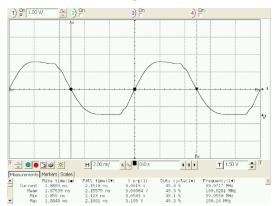


Reliability

Test Model		Perfomance
		Values
	HBM	>4000V
ESD	MM	>400V
	FI-CDM	>1000V
Latch-up	Current Test	>200mA
	Voltage Test	>5.4V

Output waveform

 $100 \mathrm{MHz}$ CL= $15 \mathrm{pF}$ VDD= $3.0 \mathrm{V}$



Notes: The document is a brief data sheet of the product.

Please contact with us by email for detailed data sheet, when needed.

KAHO Rev.3