

Overview

The high temperature version is built up by strict final test based on stable process capability. An oscillation amplifier is supplied with a voltage regulator output apart from VDD. The voltage supply separation builds low frequency variation with supply voltage VDD. ESD and latch-up test have complied with AEC-Q100 standard.

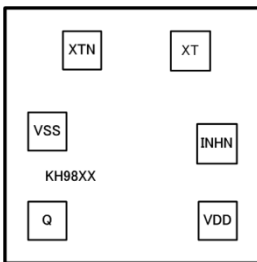
Features

Operating Voltage	2.25 to 3.63V
Operating Temperature	-40 to 125°C
Standby Mode INHN="L"	Q Output "Hi-z" Oscillation Stopped
INHN Input Voltage Level	C-MOS
Q Output Duty Level	C-MOS
Q Output Current	8mA (VDD=2.25V)
Q Output Load (Drive Capacity)	15pF (≤110MHz)
	30pF (≤ 90MHz)

Device Selection Table

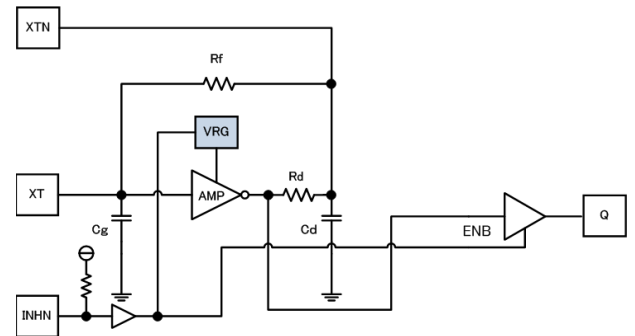
Version	Oscillation frequency (3rd Overtone)
KH9827ALAH	30 to 45MHz
KH9827ALBH	40 to 55MHz
KH9827ALCH	50 to 75MHz
KH9827ALDH	65 to 110MHz

PAD Locations



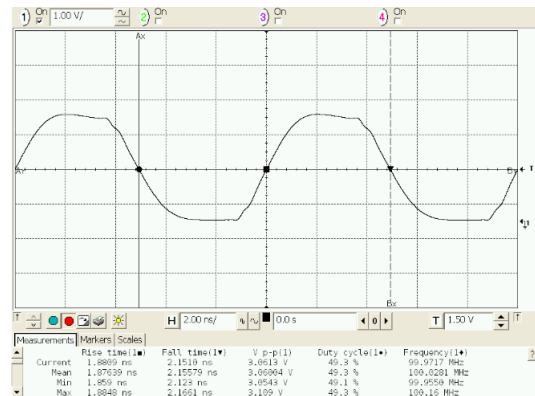
Chip Size	0.74×0.67mm
PAD Size	90×90um
Chip Thickness	130±20um
Chip Base	VSS Level

Block Diagram



Output waveform

100MHz CL=15pF VDD=3.0V



Reliability

Test Model		Performance Values
ESD	HBM	>4000V
	MM	>400V
	FI-CDM	>1000V
Latch-up	Current Test	>200mA
	Voltage Test	>5.4V

Notes: The document is a brief data sheet of the product.

Please contact with us by email for detailed data sheet, when needed.